## Week 1 - Exokernel design question

The paper describes Aegis' representation of the CPU as a linear vector, where each element corresponds to a time slice.

Design the API that Aegis would offer to libOSes to allocate time slices.

Your design should enable the construction of libOses that support an arbitrary range of application requirements (including real-time deadlines, interactive response time etc.). It should follow the Exokernel principles closely. It should not change Aegis' representation of the CPU as a linear vector of time slices

(Write your answer on this page, in no more than seven sentences.)